



Government of India, Department of Space  
Indian Institute of Space Science and Technology  
[Declared as Deemed to be University under Sec.3 of the UGC Act 1956]  
Valiamala PO, Thiruvananthapuram -695 547, Kerala



**Application for Research / Project Fellow (Online Application only)**

Indian Institute of Space Science and Technology (IIST) invites application for the following Research / Project Fellow positions to work in the different Research Projects:

Sl. No.	Category No.	Qualification / Skill	Project Title	No. of Positions
<b>Senior Research Fellow (SRF)</b>				
1	<u>SRF- 01</u>	<p>BE / B.Tech in Electronics and Communication (or subjects related to communication) and Two years of research experience in FPGA/ DSP/ Embedded Systems based applications or related areas in reputed R&amp;D institution or industry</p> <p style="text-align: center;">OR</p> <p>M.E / M.Tech in Signal Processing/ Communication Systems/Digital System Design/VLSI Design or related areas with Basic degree should be in BE / B.Tech in Electronics and Communication (or subjects related to communication).</p> <p>M.Tech should have been done through GATE.</p> <p>It is desirable that the candidates should</p> <p>(i) have good Programming Skill (ii) be willing to work in all aspects of design, development and fabrication of IRNSS Navigation receiver (iii) background in Digital Design/VLSI and knowledge in Communication Systems/Designs (iv) have worked on GNSS based receivers like IRNSS, GPS etc. (v) be willing to travel at short notice to anywhere in India for data collection and to the partner institutions (vi) be willing to learn such systems/topics from open resources</p>	Design and Development of NavIC Receiver	3

<b>Junior Research Fellow (JRF)</b>				
2	JRF-01	M.Sc in Chemistry or related areas.	Development of PZT Ceramic Foams	1
<b>Senior Project Fellow (SPF)</b>				
3	SPF-01	ME / M.Tech or equivalent degree in Power Electronics / Control Systems / Power Systems or related areas with sufficient exposure in Power Electronics or related areas: 1. Thorough understanding of conventional isolated DC-DC Converter and their Controller design in domain frequency is required. 2. Understanding of soft switching techniques is desirable. 3. Should be proficient in MATLAB / SPICE Software and setting up and debugging circuits on Bread board / general purpose PCBs.	Study of Soft Switching Topologies and Control Schemes for Isolated DC-DC Power Converters	1
4	SPF-02	ME / M.Tech in VLSI and Microsystems/ Microelectronics/ Solid State Technology or related areas. Basic degree should be in BE/ B.Tech in Electronics and Communication Engineering/ Electrical and Electronics Engineering or related areas. <b>Desirable:</b> Experience in MEMS/ Microsystems/ Nanoelectronics Design/ Fabrication of MEMS or Semiconductor device.	Development of MEMS Accelerometer with Ultra Sensitive Transductors for Space Applications.	1

**Age limit:** 35 years as on 31.01.2018 (Age relaxation as per Government of India rules).

**Remuneration per month (Consolidated):**

Category Code	Remuneration
SRF-01	Rs. 28,000/- + HRA
JRF-01	Rs. 25,000/- + HRA
SPF-01&02	Rs. 22,000/-

**General Conditions/ Instructions:**

- 1) Only Indian Nationals need apply.

- 2) All degrees mentioned above should be in FIRST CLASS.
- 3) The tenure of the position is for a period of One year or co-terminus with the project whichever is earlier. The tenure is extendable on performance and need basis subject to availability of funds.
- 4) The Institute reserves the right to terminate the appointment at any time before completion of the tenure / project, if it so decides.
- 5) Application received online only will be considered for processing under any circumstances.
- 6) The applicants will not be allowed to make any changes in the profile registration once submitted. Hence utmost care should be taken by the candidates while filling the profile.
- 7) Facility for **online submission** of application will be **available from 15.01.2018 (1500hrs onwards) to 31.01.2018 (upto1700hrs)**.
- 8) A 'No Objection Certificate' from the employer concerned is required in respect of those applicants who are employed under Central/State Government/Public Sector Undertakings/Autonomous Bodies.
- 9) Only short listed candidates will be called for written test/interview, as the case may be, based on merit.
- 10) Candidates shortlisted for written test / interview will be intimated through e-mail id mentioned in their online application.
- 11) Shortlisted candidates belonging to SC/ST/OBC/PWD should produce copies of the relevant certificates issued by the Competent Authority to that effect.
- 12) Outstation candidates called for interview will be paid 'to and fro' Second class Railway Fare by the shortest route including reservation / sleeper charges on production of proof of journey such as Railway Tickets / Bus Tickets, etc. If the candidates travel in higher class of accommodation than the one prescribed, only Second class rail fare excluding the reservation charges / sleeper charges will be paid. The applicants are advised to make their own arrangements for their stay at Thiruvananthapuram, at their cost.
- 13) No interim correspondence will be entertained.
- 14) Canvassing in any form will be a disqualification.
- 15) Government strives to have a work force which reflects gender balance and women candidates are encouraged to apply.
- 16) Candidates who are selected for the positions have to join IIST by the specified date.
- 17) Project Fellows are not entitled for hostel facility at IIST.
- 18) Candidates are advised to refer the IIST website <https://www.iist.ac.in/career/3> for any updates.

**Advt. No. IIST/Admn/RMT/01/2018 dated 15.01.2018**